

**REMARKS**

The present filing is responsive to the Office Action.

**Summary of the Response**

Claims 1, 2, 5 and 9 have been amended. New claims 11 and 12 have been added. Claims 1-12 remain pending in this application. Reexamination and reconsideration of the present application as amended are respectfully requested.

**Allowable Subject Matter**

Applicant appreciates the Examiner's indication of allowable subject matter in claims 4, 5, 7 and 8, and allowance of claim 10.

**Claim Objections**

Claim 5 is objected. Applicant amended claim 5 to correct the noted deficiency.

**Claim Rejections Under 35 USC 112**

Claim 9 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Applicant amended claim 9 to correct the noted deficiencies.

Claim Rejections Under 35 USC 102

Claims 1, 6, and 9 are rejected under 35 U.S.C. 102(b) as being anticipated by Akiyama (US Patent 6,278,426). Claims 1-3, 6 and 9 are rejected under 35 U.S.C. 102(e) as being anticipated by Kagcyama et al. (US Patent 7,221,343). These rejections are respectfully traversed.

a. Akiyama

The Examiner identified the following elements in Fig. 2 in Akiyama and alleged correspondence to the claimed elements, as follows:

- capacitor 204 corresponds to recited first capacitive device;
- capacitor 210 corresponds to recited second capacitive device;
- transistor 203 corresponds to recited first switch;
- transistor 206 corresponds to recited second switch;
- transistor 205 corresponds to recited third switch; and
- Vss 208 corresponds to recited potential source.

Applicant notes that item 210 is identified in Akiyama as a “facing electrode”, not a capacitor, let alone the recited second capacitive device. Capacitor 214 does not correspond to the recited second capacitive device, as it is not coupled to capacitor 204 (allegedly corresponding to the recited first capacitive device in claims 1 and 9) via the transistor 206 (allegedly corresponding to the recited second switch in claims 1 and 9). Capacitor 212 also does not correspond to the recited second capacitive device, as it is also not coupled to capacitor 204 (allegedly corresponding to the recited first capacitive device in claims 1 and 9) via the transistor 206 (allegedly corresponding to the recited second switch in claims 1 and 9).

11. (New): An active matrix array device as claimed in claim 1, wherein the control terminal of the third switch is a gate of a transistor.

12. (New): An electronic device as claimed in claim 9, wherein the control terminal of the third switch is a gate of a transistor.

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a. Akiyama

The Examiner identified the following elements in Fig. 2 in Akiyama and alleged correspondence to the claimed elements, as follows:

- capacitor 204 corresponds to recited first capacitive device;
- capacitor 210 corresponds to recited second capacitive device;
- transistor 203 corresponds to recited first switch;
- transistor 206 corresponds to recited second switch;
- transistor 205 corresponds to recited third switch; and
- Vss 208 corresponds to recited potential source.

Applicant notes that item 210 is identified in Akiyama as a "facing electrode", not a capacitor, let alone the recited second capacitive device. Capacitor 214 does not correspond to the recited second capacitive device, as it is not coupled to capacitor 204 (allegedly corresponding to the recited first capacitive device in claims 1 and 9) via the transistor 206 (allegedly corresponding to the recited second switch in claims 1 and 9). Capacitor 212 also does not correspond to the recited second capacitive device, as it is also not coupled to capacitor 204 (allegedly corresponding to the recited first capacitive device in claims 1 and 9) via the transistor 206 (allegedly corresponding to the recited second switch in claims 1 and 9).

Further, Akiyama does not disclose “the second capacitive device having a smaller capacitance than the first capacitive device” required by claims 1 and 9.

Still further, Akiyama does not disclosed “a third switch coupled between the first capacitive device and a potential source, the third switch having a control terminal coupled to the second capacitive device” recited in claims 1 and 9. Given the absence of correspondence of the recited “second capacitive device”, the control terminal (i.e., gate) of transistor 205 (allegedly corresponding to the recited third switch in claims 1 and 9) is not coupled to a second capacitive device required in claims 1 and 9. Instead, the gate terminal of transistor 205 is coupled to capacitor 214 and capacitor 204, which do not correspond to the recited “second capacitive device” in claims 1 and 9.

Accordingly, Akiyama does not anticipate independent claims 1 and 9, and all claims dependent therefrom.

b. Kageyama

The Examiner identified the following elements in Fig. 4 in Kageyama and alleged correspondence to the claimed elements, as follows:

- capacitor 17 corresponds to recited first capacitive device;
- capacitor 18 corresponds to recited second capacitive device;
- transistor 13 corresponds to recited first switch;
- transistor 14 corresponds to recited second switch;
- transistor 15 corresponds to recited third switch;
- E1 corresponds to recited potential source; and
- D1 corresponds to recited charging conductor

The transistor 13 (allegedly corresponding to the first switch in claims 1 and 9) does not have a data terminal connected to D1 (allegedly corresponding to the recited charging conductor) and another data terminal connected to the capacitor 17 (allegedly corresponding to the recited first capacitive device in claims 1 and 9), as is required by claims 1 and 9. Instead, the capacitor 17 is connected to D1.

Further, Kageyama does not disclose "the second capacitive device having a smaller capacitance than the first capacitive device" required by claims 1 and 9.

Still further, Kageyama does not disclose "the third switch having a control terminal coupled to the second capacitive device" recited in claims 1 and 9. The gate terminal of the transistor 15 is not connected to the capacitor 18, but instead to voltage supply P1.

Accordingly, Kageyama does not anticipate independent claims 1 and 9, and all claims dependent therefrom.

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**CONCLUSION**

In view of all the foregoing, Applicant submits that the claims pending in this application are patentable over the references of record and are in condition for allowance. Such action at an early date is earnestly solicited. **The Examiner is invited to call the undersigned representative to discuss any outstanding issues that may not have been adequately addressed in this response.**

The Assistant Commissioner is hereby authorized to charge any additional fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this transmittal and associated documents, or to credit any overpayment to **Deposit Account No. 501288** referencing the attorney docket number of this application.

Respectfully submitted,

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